



SRM Valliammai Engineering College

(An Autonomous Institution)


SRM Nagar, Kattankulathur-603203, Kancheepuram District, Tamil Nadu

TEL : 044 - 27454784 / 726, FAX : 044 - 27451504

Department of Electronics and communication Engineering

Personal Details

Name :	C.Saravanakumar	
Designation :	Assistant Professor – Selection Grade	
Educational Qualification :	M.E.,	
Experience:	16 years 5 months	
Area of Specialization :	Applied Electronics	
Date of Birth :	11/01/1981	
Date of Joining :	12/06/2006	
Email ID :	Saravanakumarc.ece@valliammai.co.in	
Contact Numbers :	044 – 27454784, 9791747592	Extn:- 8153



Educational Details

S.No	Degree	Branch/Specialization	Institution / University	Year
1	B.E.	EEE	Pallavan College of Engineering/University of Madras	2002
2	M.E.	Applied Electronics	Thanthai Periyar Government Institute of Technology/Anna University	2005
3	PhD	Information and Communication Engineering	Anna University	Pursuing

Professional Society Memberships

1. Institute of Electronics and Telecommunication Engineers(IETE): Life Member M211936
2. Indian Society for Technical Education: LM119357
3. Computer Society of India:11505887
4. International Association of Engineers(IAENG): Life Member

Publication Details

Journals:

1. "Pearson Hashing Algorithm on Hash Tables in FPGA", International Journal of All Research Education & Scientific Methods, Vol.9, Issue.3,Pg:1703-1706,2021
2. "A Competent Multiplier Architecture with Reduced Transistor Count for Radix -2 Butterfly Computation of Fast Fourier Transform", Test Engineering and Management,Vol.83,Issue.,Pg:1577 – 1581,2020
3. "Exploration of De blocking Filter and Sample Adaptive offset for HEVC Standard", International Journal of Future Generation Communication and Networking, Vol.13,Issue.2,Pg:235-243,2020
4. "PRBS Generator using FinFET", International Journal of Grid and Distributed Computing,Vol.,Issue.,Pg:2880-2887,2020
5. "VLSI Realisation of Multipliers for Signal Processing Applications", International Journal for Scientific Research & Development IJSRD,Vol.6,Issue.12,Pg:669-672,2019
6. "A NEW FANGLED SMOKE JUMPER",EPRA International Journal of Multidisciplinary Research(IJMR),Vol.5,Issue.2, 2019
7. "A Study on Algorithms for FFT computations", International Research Journal of Engineering and Technology (IRJET),Vol.5,Issue.10,Pg:1487-1489,2018
8. "A STUDY ON LOW POWER IMPLEMENTATION OF MULTIPLEXER", International Journal of Emerging Technology in Computer Science & Electronics,Vol.25,Issue.5,Pg:316-319,2018

9. "Artificial Prosthetic Hand - An Aid for Physically Challenged People", International Journal for Scientific Research & Development, Vol.6, Issue.2, Pg:882-883, 2018
10. "POWER DISSIPATION CHARACTERISTICS IN VARIOUS ADDERS", International Journal of Advance Research in Science and Engineering, Vol.7, Issue.4, Pg:434-440, 2018
11. "A SURVEY ON DIFFERENT ARCHITECTURE FOR XOR GATE", International Journal of Emerging Technology in Computer Science & Electronics, Vol.25, Issue.5, Pg:339-343, 2018
12. "Forwarding Zone Method Among Obstacles in Clustered WSN", International Journal of Emerging Technology in Computer Science & Electronics, Vol.22, Issue.2, Pg:188-191, 2016
13. "A Study On Various Structures Of Adders And To Suggest A Best Choice For Low Power Applications", International Journal of Research in Engineering and Technology, Volume: 05 Special Issue: 04, May 2016
14. "Route Optimization Technique in Clustered WSNS among Obstacles", International Journal of Advanced Engineering and Global Technology (IJAEGT), Volume 3, Issue 11, December 2015
15. "A Substrate biased Full Adder Circuit", International Journal for Research in Applied Science and Engineering Technology (IJRASET), Volume 3, Issue III, March 2015
16. "Modeling and Simulation of Hybrid circuits for Nano- Applications in PSPICE", Journal of Emerging Trends, Volume 3 No. – 1 June 2008 ISSN No: 0973-2993 Pages: 61-66

Conferences:

1. "A Constructive approach to Numerical Mapping scheme of Nucleotides for Preprocessing in Machine Learning", 12th International Conference on Cloud Computing, Data Science and Engineering - Confluence 2022 - IEEE
2. "Design Of An Automated Recurrent Neural Network For Emotional Intelligence", 4th International Conference on Smart Systems and Inventive Technology (ICSSIT 2022) - IEEE
3. "Fault diagnosis of Gate Level 2 – to – 1 Multiplexer in FinFET Technology", 2021 International Conference on System, Computation, Automation and Networking (ICSCAN) - IEEE Explore
4. "NIR spectroscopic algorithm development for glucose detection", International Conference on Innovations in Information, Embedded and Communication Systems (ICIIECS), 2015
5. "A Novel VLSI Based Control Scheme for Three phase induction Motor" at National Conference on Computational Intelligence NCCI'05 held at BIHER, Chennai

Research and Development Details

1. No. of Research Projects Completed: NIL
2. No. of Patents Filed: 2
3. No. of Ph.D candidates guided/guiding: NIL
4. No of Conferences Convened: NIL
5. No of Seminars Convened: 4

Other Particulars

Received Best Paper award for "Fault diagnosis of Gate Level 2 – to – 1 Multiplexer in FinFET Technology", 2021 International Conference on System, Computation, Automation and Networking (ICSCAN)