



SRMValliammai Engineering College

SRM Nagar, Kattankulathur-603203, Kancheepuram District, Tamil Nadu

TEL : 044 - 27454784 / 726, FAX : 044 - 27451504

Department of Electronics and Communication Engineering

Personal Details

Name :		K.Durgadevi		
Designation :		Assistant Professor(O.G)		
Educational Qualification :		M.Tech.		
Experience:		9 years		
Area of Specialization :		VLSI Design		
Date of Joining :		16.06.2014		
Email ID :		durgadevik.ece@valliammai.co.in		
Contact Numbers :		9566140234	Extn:	

Educational Details

S.No	Degree	Branch/Specialization	Institution / University	Year
1	B.E.	EEE	Anna University	2007
2	M.Tech	VLSI Design	Kalasalingam University	2009
3	PhD	Information and Communication Engineering	Anna University	Pursuing

Professional Society Memberships

1. Indian Society of Technical Education: Life Member
2. International Association of Engineers: Life Member
3. Indian Science Congress : Annual Member
4. CSI : Life Member
5. IETE : Life Member

Publication Details

Journals:

1. Hariharan,R, Harivardhan.K, Durgadevi.K, “ Automatic Face Recognition and Detection Attendance Using Wifi camera with energy saving power syatem”, IJARESM, ISSN:2455-6211, Volume 9, April 2021.
2. K.Durgadevi, Dr.R.Karthik,” ANALYSIS OF GRID CONNECTED PV SYSTEM USING LUO CONVERTER WITH MPPT FOR FAST DYNAMIC RESPONSE USING ARTIFICIAL BEE COLONY ALGORITHM” i-manager’s Journal on Power System Engineering, pp.16-22, May - July 2019.
3. R.Sathiya Priya, G.Padmini, E.Tamilselvan, K.Durgadevi , “Design and Implementation of single switch SEPIC converter for supplying LED driver circuit with PFC correction and the automation system” International Research Journal of Engineering and Technology (IRJET), Vol.6,Issue 03, pp.1728-1730, Mar 2019.
4. K.Durgadevi, “Implementation of Grid Connected PV System using LUO-FUZZY with Maximum Power Point Tracking” International Journal for Scientific Research & Development| Vol. 6, Issue 01, 2018 | ISSN (online): 2321-0613, March 2018.

Conferences:

1. K.Durgadevi, Dr.R.Karthik,” ANALYSIS OF GRID CONNECTED PV SYSTEM USING LUO CONVERTER WITH MPPT FOR FAST DYNAMIC RESPONSE USING ARTIFICIAL BEE COLONY ALGORITHM” International Conference on Smart Automation in Computer, Electrical, Electronics And

Communication Engineering (ICSA2k19)"at SRM Valliammai Engineering College, India, 6th and 7th September 2019, pp.202-206.

2. K.Durgadevi, Dr.R.Karthik, "Performance analysis of ZETA converter using classical PID and fractional order PID controller" International conference on Power, Energy and Transmission systems - 2018,at Sri Sairam Engineering College, India, 22 & 23 February 2018.

Research and Development Details

1. No. of Research Projects Completed :Nil
2. No. of Patents Filed :Nil
3. No. of Ph.D candidates guided/guiding:Nil
4. No of Conferences Convened :Nil

Other Particulars

1. Conducted a Seminar titled " Expert Lecture on Advancements in VLSI" on 11th February 2021.
2. Conducted a Seminar titled "Modern Speed Control Strategies of Electrical Motors" on 11th February 2020.
3. Conducted a Seminar titled "Power Factor Improvement Methods of Facts Devices" on 9th August 2019.
4. Conducted a Seminar titled "Recent Trends in Electric Drives" on 6th February 2019.
5. Conducted a Workshop titled "Electromagnetic Transients" on 24th September 2016.
6. Conducted a Workshop titled," Role of Power Electronics in Modern Electrical System" on 1st April 2016.
7. Conducted a Guest lecture titled," Power quality issues" on 16th March 2015.